

**AMENDMENTS TO THE CLAIMS**

This listing of claims (with current changes indicated) replaces all prior versions,  
and listings, of claims in the application:

SEP 61  
Claims 1 to 17 (Cancelled).

18. (Currently Amended) An erasing device for a liquid crystal display image, provided in a liquid crystal display device having a liquid crystal display panel whose pixels are driven by active elements, for erasing a display image on said liquid crystal display panel when a power source of a main body of said liquid crystal display device is turned OFF, comprising:

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power source OFF detecting means for circuit detecting that said power source of the main body of said liquid crystal display device is turned OFF;

a power source control circuit panel power maintaining means for maintaining, for a certain period after said power source is turned OFF, power to said liquid crystal display panel; and

an erasing circuit means for applying to all pixels in said liquid crystal display panel an OFF-level voltage, ~~within~~ during the certain period;

said liquid crystal display panel including a pixel electrode in each pixel and an opposing electrode that opposes to said pixel electrode,

said erasing means circuit including comprising:

a source driver for outputting a video signal to source lines of said liquid crystal display panel;

a source driver control circuit for controlling said source driver; and

~~a source enable signal output circuit for outputting, to the source driver control circuit, a source enable signal which is at a selecting level during the certain period;~~

an opposing electrode control circuit for outputting an opposing electrode signal to said opposing electrode,

said power source control circuit comprising a source enable signal output circuit outputting to said source driver control circuit a source enable signal which is at a selecting level during the certain period

wherein the source enable signal is inputted into said source driver control circuit which, in response to the source enable signal, ~~generates a command to causes~~ said ~~panel power maintaining means~~ source driver to apply an OFF voltage as said video signal to said pixel electrode and ~~said opposing electrode~~ to turn OFF a liquid crystal during the certain period.

19. (Cancelled)

20. (Currently Amended) The erasing device for a liquid crystal display image of Claim 18, wherein said erasing ~~means~~circuit shuts off said liquid crystal display panel entirely by making a said video signal outputted to source lines of a pixel electrode of said liquid crystal display panel and an opposing electrode signal outputted to an

opposing electrode of said liquid crystal display panel in phase at a same level.

21. (Cancelled)

22. (Cancelled)

23. (Currently Amended) The erasing device for a liquid crystal display image of Claim 20 wherein said ~~source enable signal output circuit comprises~~: a power source control circuit ~~for controlling~~, when said power source OFF detecting ~~means~~ circuit detects that said power source of said liquid crystal display device is turned OFF, controls said source driver control circuit and said opposing electrode signal control circuit so that a said video signal outputted from the source driver and the opposing electrode signal outputted from the opposing electrode signal control circuit are in phase with each other and have a same voltage.

Claims 24 to 30 (Cancelled)

31. (Original) A reflective liquid crystal display device for displaying an image by reflecting incident light from an external furnished with said display image erasing device set forth in Claim 18.

32. (Cancelled)

33. (Original) A liquid crystal display device having a GuestHost liquid crystal display panel furnished with said display image erasing device set forth in Claim 18.

Claims 34 to 40 (Cancelled).

41. (Currently Amended) The erasing device for a liquid crystal display image of Claim 18, wherein said erasing ~~means~~ circuit outputs a gate driving signal which turns ON gate lines sequentially to turn ON the active elements per line by means of a gate driver, said erasing ~~means~~ circuit also outputting a said video signal applied to pixel

electrodes and an opposing electrode signal applied to an opposing electrode of said liquid crystal panel by means of a source driver and an opposing electrode signal control circuit, respectively, both said video signal and said opposing electrode signal being applied as said voltage which turns OFF said liquid crystal.

Claims 42 to 44 (Cancelled).

45. (Currently Amended) The erasing device for the liquid crystal display image of Claim 18, wherein:

said liquid crystal display panel includes a pixel electrode which is provided in each pixel, and an opposing electrode opposing to said pixel electrode via a liquid crystal in between,

said erasing means circuit applies during the certain period a first rectangular wave signal to said pixel electrode while applying a second rectangular wave signal which is in same phase and level as those of the first rectangular wave signal to said opposing electrode.

46. (Currently Amended) The erasing device for a liquid crystal display image as set forth in Claim 18, wherein said erasing means circuit further includes comprises:

a gate driver for outputting a gate signal to gate lines of said liquid crystal display panel; and

a gate driver control circuit for controlling said gate driver,

wherein a gate enable signal, which is at a selecting level during the certain period, is inputted into said gate driver control circuit so that a gate signal is outputted to

said gate lines, using the power supplied from said ~~panel power maintaining means~~ power source control circuit.

47. (Currently Amended) The erasing device for a liquid crystal display image as set forth in Claim 46, wherein said erasing means circuit is so adopted that the gate enable signal is inputted into said gate driver as a starting signal for said gate driver.

48. (Currently Amended) The erasing device for a liquid crystal display image as set forth in Claim 47, wherein said erasing means circuit is so adopted that the gate signal is fixed at a voltage at a constant level within the certain period.

49. (Currently Amended) An erasing device for a liquid crystal display image, provided in a liquid crystal display device having a liquid crystal display panel whose pixels are driven by active elements, for erasing a display image on said liquid crystal display panel when a power source of a main body of said liquid crystal display device is turned OFF, comprising:

power source OFF detecting means ~~for~~ circuit detecting that said power source of said liquid crystal display device is turned OFF;

~~panel power maintaining means for~~ power source control circuit maintaining, for a certain period after said power source is turned OFF, power to said liquid crystal display panel;

an erasing means for circuit applying to all pixels in said liquid crystal display panel an OFF-level voltage, using the power supplied from said panel power maintaining means power source control circuit, during the certain period;

wherein said liquid display panel includes a pixel electrode in each pixel and an opposing electrode opposed to said pixel electrode, said pixel electrode and said opposing electrode sandwiching a liquid crystal, and

wherein said erasing means circuit applies rectangular wave signals, identical in terms of phase and potential, respectively into said pixel electrode and said opposing electrode during the certain period.

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